
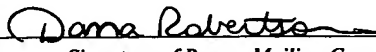


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TRANSMITTAL OF FORMAL DRAWINGS				Docket No. O2Micro 03.15	
In Re Application Of: SPENEA et al					
Application No. 10/624,295	Filing Date July 22, 2003	Examiner	Customer No. 32047	Group Art Unit 2816	Confirmation No.
Invention: CIRCUIT AND METHOD FOR TRIMMING LOCKING OF INTEGRATED CIRCUITS					
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
Transmitted herewith are: 5 sheets of formal drawing(s) for this application.					
<input type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
		Dated: August 26, 2004			
Signature					
Edmund P. Pfleger Reg. No. 41,252					
		<div>I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.3(a)] on August 26, 2004 (Date)</div> <div> Signature of Person Mailing Correspondence</div> <div>Dana Robertson Typed or Printed Name of Person Mailing Correspondence</div>			